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[Research on the three-phase load](https://www.frontiersin.org/articles/10.3389/fenrg.2024.1414084/full) [imbalance control of the active](https://www.frontiersin.org/articles/10.3389/fenrg.2024.1414084/full) [distribution network based on the](https://www.frontiersin.org/articles/10.3389/fenrg.2024.1414084/full) [distributed power](https://www.frontiersin.org/articles/10.3389/fenrg.2024.1414084/full) flow controller

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In the background of the global commitment to low-carbon renewable energy transformation, new energy sources such as distributed wind–solar storage and charging and single-phase loads, such as electric vehicles, are connected to the distribution network. Due to the asymmetric access of load and the randomness of load power consumption, the problem of three-phase imbalance is becoming more and more prominent, and the uncertainty is increasing. In this paper, by analyzing the three-phase load imbalance problem of the distribution network, starting from the cause of three-phase load imbalance, the three-phase imbalance calculation method of the distribution network is deduced. The access mode of a distributed power flow controller in the distribution network and the three-phase imbalance compensation method are studied. The control strategy of the distributed power flow controller for the three-phase imbalance control of the distribution network is proposed. The simulation results show that the proposed method can effectively control the three-phase asymmetry problem of the distribution network, the configuration is more flexible, and the capacity utilization rate is higher.

KEYWORDS

distribution network, distributed power flow controller, three-phase imbalance, arrangement, PSCAD simulation

1 Introduction

With the rapid development of smart grids, a large number of electric vehicle loads and new energy will be connected to the distribution network side. At the same time, due to the influence of season and electricity consumption habits, the single-phase load power consumption of the distribution network is random and asynchronous. If certain control measures are not taken, the medium- and low-voltage distribution network will be prone to three-phase imbalance. Uncertain single-phase new energy output increases load fluctuations and is difficult to accurately predict, which will lead to three-phase imbalance. The problem is more complex and serious [\(Song et al., 2022](#page-12-0); [Jiaqiao et al., 2022;](#page-12-1) [Ji et al., 2020](#page-12-2); [Li et al., 2018](#page-12-3); [Shaaban et al., 2015](#page-12-4); [Keane et al., 2013](#page-12-5); [Leou et al., 2014;](#page-12-6) [Borges,](#page-12-7) [2012](#page-12-7); [Singh et al., 2010\)](#page-12-8). Ensuring three-phase balance is very important for the safe and economic operation of the distribution network. Severe three-phase imbalance will increase the energy loss of the system, reduce the available capacity of feeders and transformers, cause overheating and damage of three-phase equipment, lead to feeder tripping, and even cause power grid safety accidents [\(Dong et al., 2021;](#page-12-9) [Wang](#page-12-10) [et al., 2021](#page-12-10); [Ma et al., 2016](#page-12-11); [Mostafa et al., 2013;](#page-12-12) [Ghatak et al., 2020\)](#page-12-13).

In order to effectively control the three-phase imbalance problem of the distribution network, the governance goal can be achieved by equipping with passive or active compensation equipment. [Araujolr et al. \(2018\)](#page-12-14) proposed an optimal configuration method of capacitor banks to reduce the imbalance and minimize the network loss. However, the adjustment rate of this method is limited, and it cannot cope with the rapid change in the load and the intermittent and random output fluctuation in photovoltaic power. [Abas et al. \(2020\)](#page-12-15) proposed a dynamic voltage regulator, which injects voltage phasors with a specified amplitude and frequency into the transmission line to compensate the three-phase imbalanced voltage, so as to alleviate the voltage imbalance of the three-phase four-wire distribution network. [Shahnia et al. \(2014\)](#page-12-16) proposed a distributed static synchronous compensator for the power quality control of the distribution network. By controlling the switch tube to adjust the amplitude and phase of the output voltage of the inverter AC side, the fourquadrant operation is realized, and the reactive power is quickly absorbed or emitted so as to have more flexibly control over the power quality problem of the distribution network. However, the D-STATCOM device is expensive, and the cost of installation and operation and maintenance is high. It is widely used in low-voltage distribution networks to control three-phase imbalances, but the economic cost is very high [\(Yao et al., 2019\)](#page-12-17).

As flexible AC transmission equipment with a distributed splitphase arrangement on the line, the distributed power-flow controller (DPFC) can effectively suppress the asymmetry caused by the structural parameters of the line. Owing to the distributed structure of the DPFC, each series unit distributed on the line can automatically adjust the capacity on demand and can compensate for the asymmetry of the line structure without putting in all series units so as to ensure that the series units in each work have a higher operating efficiency, better flexibility, and stronger economy ([Tang et al., 2023](#page-12-18)). Compared with D-STATCOM, the DPFC is composed of several sub-modules based on a single-phase voltage source converter. Each submodule can complete tasks such as power flow control, threephase imbalance compensation, and harmonic suppression independently or cooperatively. It has high reliability, is highly economical, and has strong flexibility and is very suitable for multi-branch and multi-section characteristics of the active distribution network. [Tang et al. \(2021\)](#page-12-19) proposed a new distributed power-flow controller topology suitable for the distribution network, and the three-loop control series side is used to realize the power flow control of the distribution network feeder. [Krishna et al. \(2016\)](#page-12-20) optimized the controller parameters of the DPFC by the fuzzy logic method, and the harmonic distortion problem of the distribution network is solved. [Rajasekhar and Babu](#page-12-21) [\(2016\)](#page-12-21) studied the ability of the DPFC to compensate for voltage sag in the case of a three-phase fault through example simulation. [Lakshmi and Jyothsna \(2016\)](#page-12-22) verified the ability of the DPFC to improve the fault current and voltage in the case of a three-phase

fault. [Tang et al. \(2019\)](#page-12-23) proposed a DPFC multi-objective coordinated control strategy considering various constraints based on the constraints of DC capacitor voltage, injection voltage, and line power flow limit of the DPFC unit. Although some studies have studied the control strategies of the DPFC system level and device level, they mainly focus on the realization of the DPFC power flow control function, and there is no relevant research on the three-phase imbalance control problem in the active distribution network scenario.

Taking the medium- and low-voltage distribution network as an example, this paper analyzes the three-phase load balance problem of an active distribution network. Starting with the cause of three-phase load imbalance, the calculation method of three-phase imbalance in the distribution network is deduced. The access mode of the distributed power-flow controller in the distribution network and the compensation method of three-phase imbalance are studied. The control strategy of the distributed power flow controller for the threephase imbalance control of the distribution network is proposed, and its effectiveness is verified by simulation.

2 Analysis of the three-phase load balance problem of the active distribution network

The structure of the distribution network is shown in [Figure 1.](#page-2-0) Due to the randomness and asymmetric access of the user electricity, the three-phase load of the distribution network is imbalanced, resulting in an imbalanced voltage or imbalanced current of the system. With the improvement in people's living standards, a large number of high-power single-phase home appliances are used, which aggravates the three-phase load imbalance problem of the low-voltage distribution network and affects the economic operation of the distribution network.

According to national standards, for low-voltage distribution networks, the current imbalance at the load common connection point is not more than 10% and the voltage is not more than 2%. The national standard for the precise definition of the three-phase imbalance degree is as follows ([Qian et al., 2019](#page-12-24)):

$$
\begin{cases}\n\varepsilon_U = U_1/U_2 \times 100\% \\
\varepsilon_I = I_1/I_2 \times 100\% \n\end{cases}.
$$
\n(1)

In the formula, ε_U and ε_I are the three-phase voltage and current imbalance degree, respectively; U_1 and U_2 are the root mean square of the positive sequence component and negative sequence component of voltage, respectively; I_1 and I_2 are the root mean square of the positive sequence component and negative sequence component of current, respectively.

$$
\begin{cases}\n\varepsilon_U = U_0 / U_1 \times 100\% \\
\varepsilon_I = I_0 / I_1 \times 100\% \n\end{cases}.
$$
\n(2)

In the formula, U_0 and I_0 are the root mean square of the zero sequence component of voltage and current, respectively.

In practical applications, the current imbalance caused by load is the most common. When the three-phase current imbalance is serious, it will cause a series of hazards such as increased line loss, reduced distribution transformer output, and increased motor loss.

In order to compare the difference of the same power line loss transmitted between the balanced system and the imbalanced system. The line impedance is Z_1 , and the system power supply voltage is symmetrical. Considering the small proportion of line loss in the total power consumption, in order to facilitate the calculation of each phase current, the line loss is included in the power consumed by the user, that is, the output power of each phase power supply is used as the load power, as shown in [Figure 2](#page-2-1).

The three-phase total complex power of the balance system is

$$
\tilde{S}_{\text{bal}} = 3UI\cos\varphi + j3UI\sin\varphi. \tag{3}
$$

In the formula, U is the effective value of the phase voltage; I is the effective value of the phase current; and φ is the impedance angle of the load.

The line loss of the balance system is

$$
\tilde{S}_{\text{bal}_\text{loss}} = 3I^2 Z_1. \tag{4}
$$

When the system load current is asymmetric, the relationship between the phase current and the sequence component is

$$
\begin{cases}\n\dot{I}_{A} = I_{1} \angle -\varphi_{1} + I_{2} \angle -\varphi_{2} \\
\dot{I}_{B} = I_{1} \angle (-\varphi_{1} - 120^{\circ}) + I_{2} \angle (-\varphi_{2} + 120^{\circ}). \\
\dot{I}_{C} = I_{1} \angle (-\varphi_{1} + 120^{\circ}) + I_{2} \angle (-\varphi_{2} - 120^{\circ})\n\end{cases}
$$
\n(5)

In the formula, I_1 is the positive sequence component of the current; I_2 is the negative sequence component of the current; φ_1 is the positive sequence component of the load impedance angle; and φ is the negative sequence component of the load impedance angle.

Then according to [Equations 1](#page-1-0)–[6,](#page-2-2) the three-phase total complex power of the imbalanced system is

$$
\tilde{S}_{\text{unbal}} = 3UI_1 \cos \varphi_1 + j3UI_1 \sin \varphi_1. \tag{6}
$$

Although the single-phase power of A, B, and C is different, the average power of the three-phase positive sequence voltage and negative sequence current is 0, that is, in the total output power, only the active and reactive power generated by the positive sequence voltage and positive sequence current. For a voltage symmetric system, if the output power of the balanced system is equal to that of the unbalanced system, $\tilde{S}_{unbal} = \tilde{S}_{bal}$, then,

$$
I_1 \cos \varphi_1 = I \cos \varphi
$$

\n
$$
I_1 \sin \varphi_1 = I \sin \varphi
$$
 (7)

[Formula 7](#page-2-3) is squared on both sides and can be obtained by accumulation

$$
I_1^2 = I^2. \tag{8}
$$

In other words, for the power supply voltage symmetrical system, the transmission power is the same, and the positive sequence component of the unbalanced system current is equal to the balanced system current. Then according to [Equation 8,](#page-2-4) the line loss of the unbalanced system is

$$
\tilde{S}_{\text{unbal_loss}} = (I_A^2 + I_B^2 + I_C^2)Z_1 = 3(I_1^2 + I_2^2)Z_1 > \tilde{S}_{\text{bal_loss}} = 3I^2Z_1.
$$
\n(9)

It can be seen that the three-phase unbalanced system will increase the line loss. In addition, the operation of the distribution transformer in the three-phase imbalanced condition will lead to a large increase in its copper consumption and an increase in the heat of the overload phase. Due to the asymmetry of the three-phase magnetic circuit, a large amount of magnetic flux leakage passes through the clamp and the oil tank, causing additional losses, resulting in very high temperature of the transformer and shortened insulation life.

The three-phase imbalance will also cause the neutral point potential of the system to shift, resulting in a low- or high-phase voltage, which will damage the lighting equipment and user lighting experience in the low-voltage distribution network, and reduce the life of household appliances. In addition, three-phase unbalance will lead to non-characteristic harmonics in AC equipment with semiconductor devices, and the voltage unbalance exceeds the design threshold.

3 Three-phase imbalance control strategy of the distributed power flow controller

3.1 Analysis of the three-phase load imbalance control principle based on the distributed power flow controller

The distributed power flow controller is composed of three single-phase sub-modules. Each sub-module can be operated and controlled independently. It has strong redundancy, can be dispersed on the line, and has strong flexibility. It is very suitable for the distribution network with a complex network structure and dense load. The topology and installation method of the sub-module of the distributed power flow controller are shown in [Figure 3](#page-3-0) and [Figure 4.](#page-4-0) The thyristor bypass switch (TBS) and the fast mechanical bypass switch are configured in the DPFC sub-module, and the action time of the bypass switch is hundreds of microseconds. When an extreme condition occurs in a phase of the distribution network, the sub-module quickly and automatically exits and no longer outputs the equivalent impedance, and the remaining sub-modules of the unfaulted phase can continue to operate.

The distributed power flow controller can be equivalent to the impedance mode, power mode, and voltage source mode. In this paper, the DPFC is used to compensate for the asymmetry of the three-phase load in the distribution network. By controlling the output voltage of each sub-module of the DPFC, the actual working voltage of the load is changed so that the three-phase line current is balanced. The equivalent circuit diagram of the distribution network

after the installation of the distributed power flow controller is shown in [Figure 5](#page-4-1), where U_A , U_B , and U_C are the three-phase line voltages, respectively; U_{seA} , U_{seB} , and U_{seC} are the equivalent voltage injected by the three sub-modules of the DPFC into the three-phase line of A, B, and C, respectively; Z_{seA} , Z_{seB} , and Z_{seC} are the equivalent impedance output by the three sub-modules of the DPFC, respectively; P_A , P_B , and P_C are the active power of the three-phase line, respectively.

[Figure 5](#page-4-1) shows that after the DPFC is installed, the three-phase line impedance is

$$
\begin{cases}\nZ_A = Z_{\text{seA}} + Z_{La} \\
Z_B = Z_{\text{seB}} + Z_{Lb} ,\\ Z_C = Z_{\text{seC}} + Z_{LC} \n\end{cases}
$$
\n(10)\n
$$
\begin{cases}\nZ_{\text{seA}} = \frac{\dot{U}_{\text{seA}}}{I_A} \\
Z_{\text{seB}} = \frac{\dot{U}_{\text{seB}}}{I_B} .\\ \nZ_{\text{seC}} = \frac{\dot{U}_{\text{seC}}}{I_C}\n\end{cases}
$$
\n(11)

Assuming that the three-phase supply voltage is balanced, by controlling the output voltage of each sub-module of the distributed power flow controller, $Z_A = Z_B = Z_C$, the three-phase load current balance of the line is realized, and the compensation vector diagram is shown in [Figure 6](#page-5-0).

3.2 Three-phase load balance control strategy based on the distributed power flow controller

From the analysis of [Section 3.1](#page-3-1), it can be seen that the DPFC can change the equivalent impedance of the three-phase

line by controlling the output voltage of the distributed power flow device to make it equal and achieve three-phase current balance when dealing with three-phase load imbalance. [Figure 5](#page-4-1) is equivalent to [Figure 7](#page-5-1); in the figure, \dot{U}_s and \dot{U}_r are the voltage at the beginning and end of the branch load, respectively. X_{L1} and X_{L2} are the impedance of the previous and next branch lines, respectively. P is the active power flowing into the branch, and P_A , P_B , and P_C are the active power of the A, B, and C three-phase lines, respectively.

[Figure 7](#page-5-1) shows that the active power of the three-phase line is

$$
\begin{cases}\nP_A = \frac{U_s U_r}{|Z_A|} \left(1 \pm \frac{U_{\text{seAi}}}{\sqrt{U_s^2 + U_r^2 - 2U_s U_r \cos \theta_{\text{sr}}} } \right) \\
P_B = \frac{U_s U_r}{|Z_B|} \left(1 \pm \frac{U_{\text{seBi}}}{\sqrt{U_s^2 + U_r^2 - 2U_s U_r \cos \theta_{\text{sr}}} } \right).\n\end{cases} (12)
$$
\n
$$
P_C = \frac{U_s U_r}{|Z_C|} \left(1 \pm \frac{U_{\text{seCi}}}{\sqrt{U_s^2 + U_r^2 - 2U_s U_r \cos \theta_{\text{sr}}} } \right)
$$

In the formula, θ_{sr} is the voltage phase difference between the head and end of the branch.

Phasor diagram based on the three-phase load imbalance compensation principle of the distributed power flow controller. load impedance, thereby achieving the line three-phase load current balance.

In order not to affect the load of each phase of the branch line, the active power command of each phase line should be

$$
P_{ref} = \max(P_A, P_B, P_C). \tag{13}
$$

Substituting [Equation 13](#page-5-2) into [Equation 12,](#page-4-2) it can be seen that the impedance value that the DPFC needs to compensate is

$$
Z_{\text{sem}} = \frac{U_s U_r \sin \delta_{sr}}{P_{\text{ref}}} - Z_{\text{Lm}}.\tag{14}
$$

In the equation, $m \in \{A, B, C\}$.

At this time, the effective value of each phase line current $|I_n|$ is

$$
|I_m| = \frac{\sqrt{(U_s - U_r \cos \delta_{sr})^2 + (U_r \sin \delta_{sr})^2}}{|Z_{\text{Lm}} + Z_{\text{sem}}|}.
$$
 (15)

Thus, the injection voltage A of the DPFC sub-module in each phase can be calculated as

$$
U_{\rm sem} = |I_m| Z_{\rm sem}.\tag{16}
$$

The accuracy of the DPFC total injection voltage instruction U_{sem} calculated by [Equations 13](#page-5-2)–[16](#page-5-3) depends on the accuracy of the

[Equation 12](#page-4-2) shows that when the three-phase load current reaches equilibrium, that is, the three-phase impedance amplitude is equal, the active power of the A, B, and C threephase lines is equal. Therefore, when the distributed power flow controller is used for load current compensation, the three-phase circuit power balance can be controlled to achieve equal three-phase

sampling of Z_{Lm} , \dot{U}_s , and \dot{U}_r . In practical applications, the above parameters will change with the actual working conditions, resulting in the DPFC being unable to make the controlled line reach the expected power flow value. Although a correction coefficient can be added according to the actual situation to correct the error, the coefficient generally depends on the operating experience, and it will be difficult to achieve good results in the case of complex operating conditions and frequent changes in the system.

In order to solve this problem, this paper proposes a power flow control strategy based on the PI controller: taking the deviation signal P_m between the given value P_{ref} of line active power flow and its actual value as input, the total injection voltage command U_{sem} of the DPFC unit is dynamically calculated by the PI controller. The specific DPFC control block diagram is shown in [Figure 8.](#page-5-4)

Through the feedback control mechanism, the DPFC can continuously change the output state and finally reach the stable state of zero static error.

When regulating the line power flow, the DPFC needs to exchange the active power in the power grid to maintain the stability of the DC capacitor voltage. At the same time, it needs to invert the corresponding compensation voltage to regulate the line power flow to reach the command value. According to [Equation](#page-3-2) [11](#page-3-2), the output voltage command of the DPFC is calculated, and the SPWM voltage of the single-phase inverter is changed so that the voltage transmitted by each sub-module of the DPFC follows the voltage command to realize the power flow control function. From [Figure 5](#page-4-1), the mathematical model of the DPFC sub-module in the dq rotating coordinate system is

$$
\left\{\n\begin{aligned}\nU_{\text{rsed}} &= U_{\text{semd}} - L_{\text{f}} \frac{\mathrm{d}I_{\text{Lmd}}}{\mathrm{d}t} + \omega L_{\text{f}} I_{\text{Lmq}} \\
U_{\text{rseq}} &= U_{\text{semq}} - L_{\text{f}} \frac{\mathrm{d}I_{\text{Lmq}}}{\mathrm{d}t} - \omega L_{\text{f}} I_{\text{Lmq}} \\
I_d &= I_{\text{Lmd}} - C_{\text{f}} \frac{\mathrm{d}U_{\text{rsed}}}{\mathrm{d}t} + \omega C_{\text{f}} U_{\text{semq}} \\
I_q &= I_{\text{Lmq}} - C_{\text{f}} \frac{\mathrm{d}U_{\text{rseq}}}{\mathrm{d}t} - \omega C_{\text{f}} U_{\text{semd}}\n\end{aligned}\n\right.\n\tag{17}
$$

The line power flow control is essential to control the output voltage of the DPFC. [Equation 17](#page-6-0) shows that the adjustment of the line power flow can be transformed into the control of the d-axis component and the q-axis component of the DPFC output voltage. In order to achieve the DPFC output voltage to the d-axis component and the q-axis component command error-free tracking, the inner loop control proposed in this paper is as follows:

$$
\begin{cases}\nU_{\text{sered}} = \left(K_{\text{p}} + \frac{K_{\text{i}}}{s}\right) \cdot \left(U_{\text{semd}}^{*} - U_{\text{semd}}\right) + \omega L_{\text{f}} I_{\text{Lmq}} \\
U_{\text{sereq}} = \left(K_{\text{p}} + \frac{K_{\text{i}}}{s}\right) \cdot \left(U_{\text{semq}}^{*} - U_{\text{semq}}\right) - \omega L_{\text{f}} I_{\text{Lmd}}\n\end{cases} \tag{18}
$$

In the formula, $\overline{U}_{\rm semd}^*$ and $\overline{U}_{\rm semq}^*$ are the command values of the d-axis component and q-axis component of the output voltage of

the DPFC sub-module, respectively. K_p and K_i are the proportional coefficient and integral coefficient of the PI controller, respectively.

Since the DPFC needs to absorb active power from the grid to maintain DC capacitor voltage stability, the outer loop control of the DPFC sub-module is expressed as

$$
\begin{cases}\nU_{\text{semd}}^* = \left(K_p + \frac{K_i}{s}\right) \cdot \left(U_{dc}^* - U_{dc}\right) \\
U_{\text{semq}}^* = U_{\text{sem}}\n\end{cases} \tag{19}
$$

According to [Formula 18](#page-6-1)–[Formula 19,](#page-6-2) the control block diagram of DPFC three-phase unbalance control can be obtained, as shown in [Figure 9.](#page-6-3)

[Formula 9](#page-2-5) shows that when the output power of the balanced system is the same as that of the unbalanced system, after the unbalanced system is adjusted to the balanced system, the loss reduction of the balanced system compared with the original unbalanced system is

$$
\Delta \tilde{S}_{\text{loss}} = 3(I_1^2 + I_2^2)Z_1 - 3I^2Z_1. \tag{20}
$$

When the three-phase unbalanced system is connected to the DPFC and the above control strategy is adopted, the DPFC further realizes the three-phase balance by changing the impedance Z1 of the line. When the DPFC is connected to the system, the output power of the balanced system and the unbalanced system is no longer the same. Compared with the original unbalanced system, the loss reduction of the balanced system is

$$
\Delta \tilde{S}_{\text{loss}} = 3\left(I_1^2 + I_2^2\right)Z_1 - 3I_1^{\prime\prime 2}Z_1^{\prime\prime}.\tag{21}
$$

In the formula, I_1'' and Z_1'' are the line current and impedance after DPFC three-phase unbalance treatment, respectively.

Structure of the three-phase imbalance control test system with the DPFC.

TABLE 1 Simulation system parameters.

TABLE 2 DPFC-specific action steps.

4 Simulation analysis

In order to verify the effectiveness of the unbalanced control strategy based on the distributed power flow controller proposed in

this paper, the distribution network system is constructed, as shown in [Figure 10](#page-7-0).

The corresponding PSCAD/EMTDC-based simulation model is shown in [Figure 11](#page-7-1). DPFC series-side units are installed on nodes 1–2, and multiple sets of single-phase converters distributed on the series side are evenly distributed on the line. The specific parameters are shown in [Table 1](#page-7-2), and the specific action steps of the DPFC are shown in [Table 2.](#page-7-3)

Before the DPFC was not put into operation, due to the asymmetry of the system structure, the line current also showed an asymmetric phenomenon, and the line current waveform is shown in [Figure 12](#page-8-0). The symmetrical component analysis shows that the positive sequence current is 0.2789 kA, the negative sequence current is 0.0068 kA, the zero sequence current is approximately 0 kA, and the asymmetry is 2.43%.

Due to the asymmetry of line current, the active power flow and reactive power flow of each phase line are not exactly the same. The DPFC has the effect of forced adjustment of the line. When the DPFC is put into operation, the changes in the active power flow and reactive power flow in each phase are shown in [Figures 13](#page-8-1), [14](#page-8-2), respectively.

[Figures 13](#page-8-1), [14](#page-8-2) show that before 2.5 s, the line power flow has been in a natural distribution state because the power flow regulation module of the series-side converter has not yet started to work. At 1.5 s, two sets of converters on the series side are put into the line. Between 1.6 s and 2.5 s, since only the third harmonic control module is put into the series side, the module only plays a role in maintaining the DC capacitor voltage of the converter, so the active power flow and reactive power flow of each phase line remain asymmetric. After the

power flow adjustment module is put into operation in 2.5 s, the active power flow and reactive power flow of A, B, and C three-phase lines smoothly reach the given values of 0.07 MW and 0.02 MVar, respectively. The response time of active power flow is approximately 0.9 s, and the response time of reactive power flow is approximately 1 s. The stabilized line current is shown in [Figure 15.](#page-9-0)

The figure shows that after the DPFC is put into operation, the three-phase currents of A, B, and C are completely symmetrical. In order to better analyze, this experiment also analyzes the symmetrical component method of the line current before and after the DPFC is put into operation. The results are shown in [Figures 16](#page-9-1)–[18](#page-10-0).

The above three diagrams show the positive sequence, negative sequence, and zero-sequence components of the line current, respectively. In the analysis of [Figure 12,](#page-8-0) the DPFC is not put into work, that is, before 0.8 s, the line asymmetry is 2.43%. In the analysis of [Figures 13](#page-8-1), [14,](#page-8-2) it is also mentioned that the line current will remain asymmetrical before the power flow regulation module on the DPFC series side is put into operation, that is, before 2.5 s. [Figure 17](#page-9-2) shows that the negative sequence current of the line was approximately 0.006 kA before 2.5 s. Only after the power flow regulation module is put into operation at 2.5 s, the negative sequence current of the line begins to decrease smoothly to 0, and the response time is close to the line power flow response time, which just shows that the power flow regulation module on the series side of the DPFC has the effect of forcing the line current to control. [Figure 16](#page-9-1) shows the curve variation in the positive sequence current of the line, and the trend is similar to the change in the active power flow of the line. This is because in the transmission line with a high power factor, the amplitude of the line current is proportional to the active power of the line. [Figure 18](#page-10-0) shows the zero-sequence component of the line current, but because the zerosequence current component of the line is very small, it can be ignored.

The above phenomena show that the DPFC can effectively improve the asymmetry of line current caused by the asymmetry of the system structure, which is all due to the arrangement strategy of single-phase distributed installation on the series side of the DPFC. Because the line structure parameters of each phase are different, the output power of each phase converter is also different. As shown in [Figures 19,](#page-10-1) [20](#page-10-2), in [Figure 19](#page-10-1), the effective value of the third harmonic voltage of the single-phase inverter of the series side A, B, and C threephase single-phase converter is stabilized at 0.0096 kV, 0.0073 kV, and 0.0090 kV, respectively, after 2.5 s. In [Figure 20,](#page-10-2) the effective values of the fundamental voltage of the single-phase converters of A, B, and C on the series side are stabilized at 0.0047 kV, 0.0044 kV, and 0.0041 kV after 2.5 s, respectively, which characterizes that the series-side A, B, and C three-phase lines are compensated to varying degrees, and finally, the current of the three-phase lines A, B, and C is symmetrical. At this time, according to [Equations 20](#page-6-4), [21,](#page-6-5) the loss reduction of the simulation system is 743.1 W, and the loss of the system is reduced, which further illustrates the effectiveness of using the DPFC for threephase imbalance control.

5 Conclusion

In this paper, the problem of three-phase load imbalance in the distribution network is analyzed, and the calculation method of the three-phase imbalance in the distribution network is deduced. The access mode and compensation method of the distributed power flow controller for three-phase imbalance control of the distribution network are given. The control strategy of the distributed power flow controller for the three-phase load imbalance control of the distribution network is proposed, and the simulation verification is carried out. The following conclusions are obtained.

- 1) The suppression of line current asymmetry is mainly the series-side unit of the DPFC-distributed phase arrangement on the line. The function of the DPFC to control the power flow of each phase is used to make the circuit of A, B, and C three-phase lines symmetrical, which verifies the feasibility of the DPFC to control three-phase asymmetry.
- 2) The ability of the DPFC to suppress three-phase asymmetry is verified, which can effectively reduce the additional loss caused by asymmetry and improve the power supply economy of the distribution network.
- 3) It is verified that the series side of the DPFC can automatically adjust the capacity according to the different compensation degrees of each phase line, which reflects the superiority of DPFC distribution. In the medium- and low-voltage distribution network with serious asymmetry, this feature of the DPFC can make the configuration of each phase capacity more flexible and the capacity utilization rate higher.

Data availability statement

The original contributions presented in the study are included in the article/Supplementary Material; further inquiries can be directed to the corresponding author.

Author contributions

GG: conceptualization, methodology, writing–original draft, and writing–review and editing. XZ: formal analysis, writing–original draft, and writing–review and editing. JW: conceptualization, methodology, writing–original draft, and writing–review and editing. ZL: conceptualization, methodology, writing–original draft, and writing–review and editing. KF: data curation, writing–original draft, and writing–review and editing. FP: formal analysis, writing–original draft, and writing–review and editing. DG: formal analysis, writing–original draft, and writing–review and editing. AT: conceptualization, data curation, methodology, writing–original draft, and writing–review and editing. CX: conceptualization, writing–original draft, and writing–review and editing. ZW: data curation, writing–original draft, and writing–review and editing.

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Conflict of interest

Authors GG and XZ were employed by Guangdong Power Grid Co.

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