

# Passivity Enhancement Strategy of Grid-Connected Inverter System Based on the Adaptive Active Damper

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The grid-connected inverter system may be unstable when connected to a weak grid, and installing an active damper as a virtual resistor at the PCC is convenient for stabilizing the system. This article proposes a passivity enhancement strategy for the grid-connected inverter system via the adaptive active damper. Furthermore, the admittances of the grid-connected inverter system with the active damper are derived, and the real parts of the admittances of the whole system have been calculated. And the admittance measurement results obtained by the frequency scanning method approximately match the theoretical results. Additionally, the virtual resistor's stability region is discussed, the bandwidth of the phase-locked loop is recommended, ensuring the stabilizing effect of the active damper under the weak grid. And the online resonance frequency detection algorithm guarantees the passivity enhancement of the active damper when grid impedance changes. Finally, controller hardware-in-the-loop tests validate the theoretical analysis of passivity enhancement based on the active damper.

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# **1 INTRODUCTION**

The grid-connected inverter (GCI) with the LCL filter is an essential interface between the distributed renewable energy and the utility grid (Li et al., 2017; Wang and Blaabjerg, 2019). However, the LCL filter is a third-order filter with inherent resonance characteristics, and the interaction between the grid impedance and the converters may trigger high-frequency oscillations (Akhavan et al., 2020; Kang, 2020; Chen et al., 2021). To mitigate harmonic oscillations and improve system stability, the active damper is proposed in Wang et al. (2014) to be installed at the point of common coupling (PCC).

Previous work investigates many topologies suitable for the active damper to achieve impedance shaping and resonance suppression. In Wang et al. (2014a) and Wang et al. (2014b), the L filter is adopted for the active damper. In Cheng et al. (2020) and Jia et al. (2018), the active damper is implemented with the LCL filter for lower costs and size. In Zhou et al. (2018), the active damper adopts the LC filter since the voltage source converter is easier to virtualize smaller harmonic impedance than the current source converter. In Bai et al. (2017), Wang et al. (2015), and Bai et al. (2016), active dampers are equipped with the series LC filter, reducing their power ratings significantly. In Zhao et al. (2019) and Lu et al. (2016), instead of paralleling at the PCC, the adaptive stabilizer is connected in series to reshape the grid impedance. In Ni et al. (2021), a unified active damper combining a parallel and serial converter is proposed to reshape the equivalent impedance and enhance the stability of the GCI system. In Guo et al. (2019) and Lin and Ruan

(2019), instead of installing an extra converter at the PCC, the active damper is achieved by adding a damping branch on the control algorithm of the existing grid-connected inverter, requiring an upgrade in the control program's existing inverters.

Existing research also focuses on the active damper's control strategy, especially on virtual impedance emulation and resonance detection. In Jia et al. (2018), the variable virtual resistor-based adaptive resonance suppression control strategy of the active damper is proposed. In Li et al. (2020), the lowest value of the virtual resistor is designed to avoid the overmodulation problem. In Cheng et al. (2020), an RL-type active damper is proposed to cope with the wideband oscillation, avoiding the instability caused by the system parameter variations. In Zhou et al. (2017), an active capacitor converter is proposed to shape the equivalent impedance at the PCC by emulating paralleled capacitor. However, the virtual capacitor requires a differential operation, inevitably amplifying the computational noises. In Jin et al. (2020), a cascaded SOGI-FLL with pre-filter is adopted to detect the unstable frequency region in real-time.

Both impedance-based and passivity-based stability criteria can be utilized to analyze the stability of the GCI system (Harnefors et al., 2016; Sun, 2011; Xiong et al., 2020; Wu et al., 2020; Liao et al., 2020). For the paralleled inverter system, the passivity-based criterion is more intuitive since it is possible to analyze the frequency band where the instability occurs, so the parameters of the whole system can be designed quantitatively (Han et al., 2022; Qian et al., 2020). Noting that the passivity is a sufficient yet unnecessary condition to ensure the system is stable, the relatively conservative analysis result can provide a large stability margin of the system (Yoon et al., 2016). This article is organized as follows. **Section 2** introduces the GCI system with the active damper. In **Section 3**, the admittances of the grid-connected inverter system with the active damper are modeled and measured by the frequency scanning method. The tradeoff design of the active damper is achieved in **Section 4**. The passivity enhancement result by the adaptive active damper is presented in **Section 5**. The controller hardware-in-the-loop (CHIL) tests results are presented in **Section 6**. Conclusions are drawn in **Section 7**.

### **2 SYSTEM DESCRIPTION**

# 2.1 Grid-Connected Inverter System With the Active Damper

As shown in Figure 1, this article analyses a GCI system with the active damper under the weak grid. The active damper is simulated as a resistor connected in parallel at the PCC, damping the resonance between the grid-connected inverter and the grid, stabilizing the GCI system (Zhang et al., 2016). The LCL filter of the GCI comprises the inverter-side inductor  $L_{fi}$ , the filter capacitor  $C_{fi}$ , and the grid-side inductor  $L_{ei}$ . For simplicity, its DC-link voltage  $V_{dci}$  is assumed to be constant. The second-order generalized integrator-based phaselocked loop (SOGI-PLL) is adopted for real-time grid synchronization. For the active damper, the resonant component  $u_{pcc-r}$  extracted by the adaptive resonance detection method is divided by the virtual resistor value  $R_e$  to obtain the harmonic current reference  $i_{AD\_r\_ref}$ . The active damper's output current,  $i_{AD}$ , is tracked through the current controller  $G_{ca}$ , where the PR controller is utilized. The DC voltage controller  $G_{dca}$  maintains the DC side voltage constant. The  $H_{ica}$  is the capacitor current feedback coefficient.





### 3 ADMITTANCE MODELING OF GRID-CONNECTED INVERTER SYSTEM WITH THE ACTIVE DAMPER

## **3.1 Admittance Modeling of the Grid-Connected Inverter**

The mathematical model of the GCI could be derived as **Figure 2A**. The grid-connected inverter contains two control loops, the path of the current control loop is from the  $i_{inv\_ref}$  to  $i_{inv\_ref}$ . The simultaneous sampling mode is adopted in this study. Hence, the sampling frequency equals the switching frequency. The time delay is 1.5 sampling period, so the  $G_{di} = e^{-1.5sTswi}$ .  $K_{pwm}$  represents the gain of the inverter bridge.  $T_{PLL}$  stands for the transfer function model of the phase-locked loop from the  $u_{pcc}$  to  $cos\theta$ . The  $H_{iCi}$  is the capacitor current feedback coefficient. The  $G_{ci}$  is the current controller and could be expressed as **Eq. 1**.

$$G_{ci}(s) = K_{pi} + K_{ri} \frac{2\omega_{ci}s}{s^2 + 2\omega_{ci}s + \omega_0^2}.$$
 (1)

By executing a similar transformation in Wang et al. (2010) and Yang et al. (2014), Figure 2A can be transformed into and equivalent parts can be expressed as Eqs 2,3.





$$G_{x1} = \frac{G_{ci}K_{PWM}G_{di}}{s^2 L_{fi}C_{fi} + sK_{PWM}H_{iCi}C_{fi}G_{di} + 1},$$
(2)

$$G_{x2} = \frac{s^2 L_{fi} C_{fi} + s K_{PWM} H_{iCi} C_{fi} G_{di} + 1}{s^3 L_{fi} L_{gi} C_{fi} + s^2 K_{PWM} H_{iCi} L_{gi} C_{fi} G_{di} + s \left(L_{fi} + L_{gi}\right)}.$$
 (3)

According to Figure 2C, the output current of the gridconnected inverter can be derived as

$$i_{inv} = -u_{pcc} \left( Y_{coni} + Y_{PLLi} \right), \tag{4}$$

where  $Y_{PLLi}$  and  $Y_{coni}$  are the equivalent admittances of the PLL and current loop and can be expressed as



$$Y_{coni} = \frac{1 + sC_{fi}K_{PWM}G_{di}H_{ici} + s^2L_{fi}C_{fi}}{\Delta_i},$$
(5)

$$Y_{PLLi} = \frac{-I_{inv\_ref}T_{PLL}G_{ci}K_{PWM}G_{di}}{\Delta_i}.$$
(6)

In which,  $\Delta_i$  is the characteristic equation of the gridconnected inverter and can be derived as

$$\Delta_{i} = s^{3} L_{fi} L_{gi} C_{fi} + s^{2} L_{gi} C_{fi} K_{pwm} G_{di} H_{ici} + s \left( L_{fi} + L_{gi} \right)$$
  
+  $G_{ci} K_{pwm} G_{di}.$  (7)

In Zhang et al. (2015b), Chen et al. (2017), and Xu et al. (2019), it has been proven that the single-phase SOGI-PLL shown in **Figure 1** has the small-signal transfer function from  $u_{pcc}$  to the  $cos\theta$  can be expressed as **Eq. 8**. In  $T_{PLL}$ , the  $U_m$  stands for the amplitude of  $u_{pcc}$  and  $\omega_0$  is the fundamental frequency, the  $G_{PI}$  denotes the PI controller.

$$T_{PLL}(s) = \frac{0.5G_{PI}(s - j\omega_0)G_{OSG-\alpha}(s)}{s - j\omega_0 + U_m G_{PI}(s - j\omega_0)}.$$
 (8)

In which the  $G_{OSG-\alpha}$  stands for the transfer function from  $u_{pcc}$  to  $u_{\alpha}$  and can be expressed as Eq. 9.

$$G_{\text{OSG}-\alpha}(s) = \frac{k\omega_0 s}{s^2 + k\omega_0 s + \omega_0^2}.$$
(9)

TABLE 1 | Electrical and control parameter of the active damper.

Parameter		Value
Damping branch	R <sub>e</sub>	6Ω
	Q	2.5
	γ	1
	$\omega_{res\_initial}$	1,400*2*pi (rad/s)
Power stage	L <sub>fa</sub>	0.8mH
	$L_{ga}$	0.5mH
	C <sub>fa</sub>	2 µF
	V <sub>dca</sub>	425V
	C <sub>dca</sub>	1,500 µF
Current loop	K <sub>pa</sub>	15
	K <sub>ra</sub>	65
	H <sub>ica</sub>	18
	f <sub>swa</sub>	50 kHz
DC-link voltage loop	K <sub>dcp</sub>	0.1
	K <sub>dci</sub>	0.2
Phase-locked loop	K <sub>pllp</sub>	1.8388
	K <sub>plli</sub>	525.916

TABLE 2 | Parameters of the grid-connected inverter system.

Parameter		Value
Weak grid	U <sub>m</sub>	311 V
	Ls	1/2/4/16 mH
Power stage	L <sub>fi</sub>	1.9 mH
	Lai	1.6 mH
	$\tilde{C_{fi}}$	15 µF
	V <sub>dci</sub>	425 V
Current loop	K <sub>pi</sub>	8
	K <sub>ri</sub>	800
	H <sub>ici</sub>	1
	f <sub>swi</sub>	10 kHz
	K <sub>pwm</sub>	1
	l <sub>inv_ref</sub>	30 A
Phase-locked loop	K <sub>pllp</sub>	1.8388
	K <sub>plli</sub>	525.916

# 3.2 Admittance Modeling of the Active Damper

As shown in **Figure 2B**, the active damper has one more damping branch to imitate the virtual resistor for harmonic resonance suppression. Likewise, **Figure 2B** can be simplified as **Figure 2D**. The expressions of the admittances of the active damper could be derived as **Eqs 10–13**.

$$i_{AD} = -u_{pcc} \left( Y_{cona} + Y_{PLLa} + Y_a \right), \tag{10}$$

$$Y_a = \frac{G_{damp} K_{PWM} G_{da} G_{ca}}{\Delta_a},\tag{11}$$

$$Y_{PLLa} = \frac{-T_{PLL}I_{AD\_1\_ref}G_{ca}K_{PWM}G_{da}}{\Delta_a},$$
(12)

$$Y_{cona} = \frac{1 + sC_{fa}K_{PWM}G_{da}H_{ica} + s^2L_{fa}C_{fa}}{\Delta_a}.$$
 (13)



FIGURE 5 | Real part of admittances versus frequency of GCI system with the active damper. (A) Theoretical analysis result. (B) Frequency scanning result.



Adaptive Active Damper

The characteristic equation can be expressed as Eq. 14.

$$\Delta_a = s^3 L_{fa} L_{ga} C_{fa} + s^2 L_{ga} C_{fa} K_{pwm} G_{da} H_{ica} + s \left( L_{fa} + L_{ga} \right) + G_{ca} K_{pwm} G_{da}.$$
(14)

The current loop of the active damper could be derived as

$$T_{AD}(s) = \frac{G_{ca}K_{PWM}G_{da}}{s^{3}L_{fa}L_{ga}C_{fa} + s^{2}K_{PWM}H_{ica}L_{ga}C_{fa}G_{da} + s(L_{fa} + L_{ga})}.$$
(15)

The damping branch of the active damper could be expressed as Eq. 16, where  $R_e$  is the virtual resistor. As for the bandpass filter shown in Eq. 17, the quality factor Q is chosen as 2.5 to ensure a wide range of resonance suppression.

$$G_{damp} = \frac{G_{band pass}}{R_e}.$$
 (16)

$$G_{bandpass}(s) = \frac{\frac{\omega_{res}}{Q}s}{s^2 + \frac{\omega_{res}}{Q}s + \omega_{res}^2}.$$
 (17)

# **3.3 Admittance Shaping Strategy Based on Active Damper**

As shown in **Figure 3**, after installing the active damper at the PCC, the inverter-side admittance could be reshaped as  $Y_{sum}$ . The passivity of the whole system can be guaranteed when the reshaped admittance  $Y_{sum}$  is passive, that is  $-90^{\circ} \le \angle Y_{sum}$  (j $\omega$ )  $\le 90^{\circ}$  in the whole frequency range (Ma et al., 2020; Zhao et al., 2021).

# 4 TRADEOFF DESIGN OF THE ACTIVE DAMPER

### 4.1 Critical Frequencies Design of the Active Damper

To ensure the harmonic resonance suppression capability of the active damper, related critical frequencies should be appropriately designed. As shown in **Eq. 18**, the LCL resonance frequency of the active damper, the  $f_{resa}$  should be lower than  $f_{swa}/6$  to guarantee stability under weak grid conditions according to Pan et al. (2015). The cutoff frequency of the current loop  $f_{ca}$  should be higher than the potential maximum resonance frequency of the grid-connected inverters  $f_{res_inv\_max}$  to guarantee the resonances suppression effect. In this study, the  $f_{res_inv\_max}$  is chosen as 2 kHz. Also,  $f_{ca}$  should be lower than  $f_{swa}/10$  to avoid the influence of switching harmonics on the current loop. The  $f_{ca}$  can be set at around 0.3 times of the LCL filter resonance frequency  $f_{resa}$  to ensure enough phase margin (Tang et al., 2012, Wang et al., 2014c).

$$\begin{aligned} f_{res\_inv\_max} &\leq f_{ca} \approx 0.3 f_{res\_a} < 0.1 f_{swa} \\ f_{res\_a} < f_{swa} / 6 \end{aligned} .$$



# 4.2 Electrical Parameters Design of the Active Damper

The implementation cost of the active damper is directly related to its power rating. It is determined by the pre-defined smallest virtual resistor it intends to imitate, namely the  $R_{e_min}$  and the maximum value of the ratio of the target resonant component to be damped to the fundamental component of the PCC voltage and, namely, the  $\lambda_{r_max}$ . The apparent nominal power of the active damper, the  $S_{AD}$  could be calculated as **Eq. 19**.

$$S_{AD} = \frac{\lambda_{r_{-}\max}V_n^2}{R_{e_{-}\min}}.$$
(19)

Assuming the  $R_{e\_min} = 5\Omega$ ,  $\lambda_{r\_max} = 10\%$ , and  $V_n = 220V$ , it is calculated that  $S_{AD\_min} \approx 1$  kVA from **Eq. 19**. The nominal current  $I_{ADn}$  of the active damper could be derived as **Eq. 20** and could be calculated as 4.4A.

$$I_{ADn} = \frac{S_{AD\_\min}}{V_n}.$$
 (20)

The expression of the filter capacitor could be derived as Eq. 21, in which the  $\lambda_C$  is the ratio of the reactive power introduced by the filter capacitor to the power rating of the active damper.

$$C_{fa} \le \lambda_c \frac{S_{AD\_\min}}{\omega_0 V_n^2}.$$
(21)

Assuming  $\lambda_c = 0.05$ , the value of  $C_{fa}$  is limited to less than 3.2 µF to reduce the current conduction losses of the IGBT. In this study, the  $C_{fa}$  is chosen as 2 µF. Considering the constraint of (**Eq. 18**), the  $L_{fa}$  is chosen as 0.8 mH, the  $L_{ga}$  is selected as 0.5 mH, and the  $f_{resa}$  is 6416 Hz according to **Eq. 22**.

$$f_{resa} = \frac{1}{2\pi} \sqrt{\frac{L_{fa} + L_{ga}}{L_{fa}L_{ga}C_{fa}}}.$$
 (22)

# 4.3 Current Loop Design of the Active Damper

In Bai et al. (2017), it has been verified that resonance suppression can still be achieved even if the active damper only adopts the proportional



FIGURE 8 | Schematic diagram and setup of the CHIL test. (A) Schematic diagram of the CHIL test. (B) Setup of the CHIL test

controller. According to Ruan et al. (2017), the capacitor branch could be approximated to open circuit when the frequency is equal to or lower than the  $f_{ca}$ , so **Eq. 15** could be simplified as

$$|T_{AD}(s)| \approx \left| \frac{G_{ca} K_{PWM} G_{da}}{s \left( L_{fa} + L_{ga} \right)} \right|.$$
(23)

Since the resonant part has little effect on the high-frequency components, the current controller  $G_{ca}$  can be approximated to  $K_{pa}$  at  $f_{ca}$  and the frequencies are higher than  $f_{ca}$ , that is  $|G_{ca}(j2\pi f_{ca})| = 1$ . Hence the proportional coefficient could be yielded (24), considering that the current loop gain has unit magnitude at  $f_{ca}$  (Parker et al., 2014), that is  $|T_{AD}(j2\pi f_{ca})| = 1$ .

$$K_{pa} \approx \frac{2\pi f_{ca} \left( L_{fa} + L_{ga} \right)}{K_{pwm}}.$$
 (24)

To ensure the effective damping effect of the potential resonances, let  $f_{ca} = f_{res\_inv\_max}$  and it could be calculated that  $K_{pa} = 16$  according to **Eqs 18–24**. To increase the phase margin, the  $K_{ra}$  of the PR controller can be chosen lower.

#### 4.4 PLL Design of the Active Damper

Under weak grid conditions, a relatively low bandwidth of PLL could guarantee the stability of the GCI system (Zou et al., 2021).



According to Han et al. (2022) and Zhang et al. (2015a), considering PLL bandwidth, the PI parameters of PLL could be calculated as

$$k_{plli} = \omega_{BW}^2 \frac{-2\xi^2 + \sqrt{1 + 4\xi^4}}{U_m},$$
(25)

$$k_{pllp} = 2\xi \sqrt{\frac{k_{i,PLL}}{U_m}}.$$
 (26)

According to **Eqs 8**, **9**, **12**, **25**, **26**, when the parameters of **Table 2**, when  $\xi = 0.707$ ,  $I_{AD\_1\_ref} = 10A$ , the real part of the output admittance of the SOGI-PLL of the active damper versus frequency and bandwidth of the SOGI-PLL can be depicted in **Figure 4**.

**Figure 4** indicates that as the non-passive region enlarges, the bandwidth of the SOGI-PLL increases. In this study, the bandwidth of the PLL is selected as 100 Hz to ensure the passivity and stability of the active damper at the low-frequency range.

# 5 PASSIVITY ENHANCEMENT STRATEGY BASED ON THE ACTIVE DAMPER 5.1 Relationship Between the Passivity and Stability

According to Chen et al. (2020) and Cheng et al. (2020), if the reshaped admittance  $Y_{sum}$  shown in **Figure 3** has a positive real



part in the whole frequency range, the GCI system under the weak grid is passive and stable. With parameters of **Tables 1**, **2**, the theoretical passivity improvement provided by an active damper is shown in **Figure 5A**. After admittance shaping, the  $Y_{sum}$  has a positive real part, except for fundamental frequency, indicating high-frequency harmonic resonances can be well suppressed. As for the non-passive region introduced by the PLL at around 50 Hz, it can hardly be avoided without reducing the rated power of the GCI since the PLL has a negative impact on the passivity (Harnefors et al., 2007; Harnefors et al., 2016). It should be noted that  $Y_{cona}$  can also compensate for the non-passive region, contributing to the stability of the GCI system.

The measured real parts of admittances of the grid-connected inverter and the active damper are shown in **Figure 5B**. Measurement of admittances of the GCI system with the active damper is carried out via the impedance analysis toolbox in the PLECS platform. The output admittances can be calculated by injecting the perturbation current at the PCC and measuring the response voltage signal in the time domain. Comparing **Figures 5A,B** with, the real parts of admittances of the grid-connected inverter and the active damper given by mathematical models roughly matched with corresponding measured results, proving the correctness of the theoretical analysis. Therefore, the analytical models can perform the passivity-based stability investigations of the GCI system with



the active damper. As shown in the frequency sweeping results, the passivity of the GCI system is greatly enhanced by the active damper. The non-passive region of the grid-connected inverter has been dramatically compensated, and the risk of highfrequency oscillations has been reduced.

# 5.2 Stability Region-Based Damping Branch Design

To avoid overmodulation, the lowest limit of the virtual resistor  $R_e$  could be calculated as  $R_e \ge 4.46$  according to **Eq. 27** and parameters of **Table 1**.

$$U_{dca} - \frac{U_m}{K_{pwm}} \ge \frac{2\pi f_{res\_inv\_max} \left( L_{fa} + L_{ga} \right) U_m \lambda_{r\_max}}{R_e}.$$
 (27)

As the effect of PLL on the passivity could be neglected in the high-frequency range according to **Figure 5**, the reshaped inverter-side admittance  $Y_{sum}$  could be approximated to  $\{Y_a + Y_{coni} + Y_{cona}\}$ . So high-frequency non-passive region is mainly related to the real part of  $\{Y_a + Y_{coni} + Y_{cona}\}$ , so the admittance of the PLL could be ignored when analyzing the high-frequency oscillations.

According to Eqs. 11–13 and parameters in Table 1, the real part of the equivalent admittances of the active damper versus  $R_e$ 



and frequency is shown in **Figure 6**. When the  $R_e$  is too small, the Re{ $Y_a + Y_{coni} + Y_{cona}$ } is negative at frequencies higher than the objective frequency band, implying the risk of high-frequency oscillation. So a too-small  $R_e$  enhances the passivity of the objective frequency band at the cost of reducing the passivity of the GCI system at higher frequencies.

The selection of the  $R_e$  is a tradeoff between the passivity enhancement of the grid-connected inverter and the active damper itself. And in this study,  $R_e$  is chosen as  $6\Omega$ .

## 5.3 SOGI-FLL-Based Adaptive Resonance Detection Method

The SOGI-FLL-based resonance detection algorithm in **Figure 7** can trace the resonant frequency in real-time (Cao et al., 2018; Jin et al., 2020). The SOGI module in SOGI-FLL also serves as the bandpass filter corresponding to **Eq. 17**. Before the system starts up, the initial value of  $\omega_{res}$ , the  $\omega_{res\_initial}$  should be set. The saturation of the integrator outputting  $\omega_{res}$  is set to prevent the SOGI-FLL from tracking low order or switching harmonics. The  $\omega_{res}$  is limited to  $2\pi^*1,000$  to  $2\pi^*2000$  (rad/s), and the  $\gamma$  is set relatively small for accurate resonance frequency detection.

### **6 VERIFICATIONS**

#### 6.1 CHIL Platform Setup

The CHIL system is essential for verifying controllers and implementing algorithms, especially under risky test conditions (Bontemps et al., 2021). This study implemented the grid-connected inverter and the active damper algorithms in the Ti 28379D MCU controller with a sampling time of 20  $\mu$ s. The hardware system was emulated in the RT-BOX 1, where the simulation time step is 3  $\mu$ s. Due to the hardware limitations, only one grid-connected inverter is emulated in the CHIL test. The switch "SW" is employed to carry out the plug-in and removal test of the active damper. It should be noted that "SW" is paralleled with a bypass resistor " $R_{sw}$ " with the value of 10 k $\Omega$  since the ideal switch can lead to numerical error in the CHIL test.

The schematic diagram and setup of the CHIL test are shown in **Figure 8**, and the DSP controller is connected to the RT-BOX via the interface PCB board. The switch "SW" shown in **Figure 8** is employed to achieve the active damper's plug-in and removal.

The control and electrical parameters of the grid-connected inverter system and the active damper are shown in **Table 1**.

The control and electrical parameters of the GCI system under the weak grid are shown in **Table 2**.

# 6.2 Dynamic performance of plugging in and removing the active damper.

As shown in **Figure 9A**, when the switch is open, the phenomenon of resonance occurs, and the whole system becomes unstable without the active damper. When the "SW" is closed, the resonant components of both PCC voltage and grid-current are quickly attenuated, proving the passivity enhancement effect of the active damper. The active damper absorbs the harmonic resonance components and returns the power to the grid at the fundamental frequency. The FFT analysis of the PCC voltage when the resonance occurs is shown in **Figure 9B**. The resonance frequency is roughly 1.1 kHz, which equals to the resonance frequency of the grid-connected inverter when  $L_s = 4$  mH, validating the analysis in **Figure 5**.

### 6.3 Adaptive Resonance Detection Effect

The dynamic resonance frequency estimation effect of the SOGI-FLL is shown in **Figure 10**, where the "SW" is open. When the grid-side inductance suddenly decreases from 4 mH to 2 mH, the



detected resonance frequency by the SOGI-FLL increases accordingly, proved by the FFT analysis.

## 6.4 Stability-region of the virtual resistor

**Figure 11A** shows the dynamic performance of the active damper when  $R_e$  changes from 6 to 1.5  $\Omega$  when  $L_s = 2$  mH, indicating that a too low  $R_e$  can trigger the harmonic resonance of the active damper itself, resulting in instability. The FFT analysis shown in **Figure 11B** shows a resonance peak at around 4.5 kHz, the resonance frequency of the LCL filter of the active damper influenced by the  $L_s$ . This indicates that a too-small virtual resistor can bring a new non-passive region, verifying the theoretical analysis shown in **Figure 6**.

# 6.5 Dynamic Performance of Active Damper when Grid Impedance Sudden Changes

**Figure 12** shows the dynamic performance of the GCI system with the active damper when the grid impedance suddenly changes from 1 mH to 16 mH. The GCI system with the active damper remains stable after the impedance increases, and the transient process is relatively short.

# 6.6 Effect of Bandwidths of PLL on the Stability of GCI System.

Comparisons of the start-up process of the active damper with different bandwidths of the PLL are shown in Figure 13.  $L_s$  is

chosen as 16 mH, corresponding to SCR = 2.06. When an unnecessarily high bandwidth of PLL of the active damper is adopted, the system will likely become unstable since the non-passive region enlarges as PLL bandwidth increases, verifying the theoretical analysis shown in **Figure 4**.

## **7 CONCLUSION**

This article proposes a passivity enhancement strategy of the grid-connected inverter system via the adaptive active damper. The mathematical models of admittances of the GCI system with the active damper under the weak grid are derived. The admittances of the grid-connected inverter and the active damper have been measured by the frequency scanning method, verifying previous mathematical models. The tradeoff design of the active damper is carried out to achieve high conductance for wideband damping, and the bandwidth selection of the PLL of the active damper is recommended. The online resonance frequency detection is achieved, ensuring the stabilizing effect of the active damper under different grid impedances. The stability region of the virtual resistor is discussed to avoid overmodulation and the high-frequency oscillations of the active damper itself. Finally, the CHIL tests verified the theoretical analyses, proving the

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passivity enhancement effect of the grid-connected inverter system by the active damper.

### DATA AVAILABILITY STATEMENT

The raw data supporting the conclusion of this article will be made available by the authors, without undue reservation.

### AUTHOR CONTRIBUTIONS

CS contributed to the conception and design of the proposed strategy. All authors wrote and edited the manuscript.

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